

### REMARKS

Prior to entry of this paper, Claims 1-34 were pending. Claims 1-34 were rejected. No amendments are made in this paper. Claims 1-34 are currently pending. For at least the following reasons, Applicant respectfully submits that each of the presently pending claims is in condition for allowance.

### Claims 1-34

Claims 1-34 were rejected under 35 U.S.C. § 103(a) as being unpatentable over Matsumura (U.S. Patent Publication No. 2004/0174206) hereafter “Matsumura” in view of La Rosa (U.S. Patent No. 6,897,689) hereafter “La Rosa”. Each of these rejections is respectfully traversed.

First, Claim 1 is respectfully submitted to be allowable at least because neither Matsumura nor La Rosa, singly or in combination, discloses, teaches, or suggests, “the hysteresis-and-output sensor circuit is arranged to disable a hysteresis at power up”, as recited in Applicant’s Claim 1.

The circuit of Matsumura discusses a voltage detecting circuit for detecting a specific voltage that is capable of adjusting a hysteresis voltage. However, it is respectfully submitted that the circuit of Matsumura fails to teach a hysteresis-and-output sensor circuit is arranged to **disable a hysteresis at power up**. Further, it is respectfully submitted that the circuit of La Rosa fails to cure the deficiency of Matsumura. The circuit of La Rosa is a programmable POR circuit with two switching thresholds. Even if the POR circuit of La Rosa were used in Matsumura, the circuit of Matsumura still would not disable POR at power-up. There is no apparent reason or suggestion that the hysteresis would be based on power-up in any way. Accordingly, even if the circuit of Matsumura incorporated the POR of Matsumura, the combination would still fail to disable hysteresis at power up.

Essentially, it is respectfully submitted that the Office has cited a circuit with adjustable hysteresis and a circuit that includes a POR circuit, and asserts that it would be obvious to combine the two to make the claims obvious because it would be obvious to include a POR in the circuit with adjustable hysteresis. However, even assuming arguendo that it would be obvious to include a POR circuit in Matsumura, that would not make it obvious to disable the hysteresis at power up. It is respectfully submitted that there is no apparent reason in the asserted references to modify the

circuit of Matsumura to disable the hysteresis at power up. Incorporating a POR circuit in Matsumura would not result in the circuit of Matsumura disabling hysteresis at power up.

Second, Claim 1 is respectfully submitted to be allowable at least because neither Matsumura nor La Rosa, singly or in combination, discloses, teaches, or suggests, “a gate circuit that is arranged to provide an output signal by gating a gate input signal subject to control by a gate control signal, wherein the gate input signal is based at least in part on the trigger signal, and wherein the gate control signal is based at least in part on the power-on-reset signal”, as recited in Applicant’s Claim 1.

Apparently, the Office’s contention is that, in the combination, the POR circuit of La Rosa would be incorporated into the circuit of Matsumura, and further that the POR signal would be used as the reset signal 52 in the circuit of FIG. 2 of Matsumura. However, there is simply no apparent reason, disclosure, teaching, or suggestion that reset signal 52 of FIG. 4 of Matsumura be substituted with a POR signal, from La Rosa or any POR signal at all. The Office Action states, “It would have obvious at the time the invention was made to a person of ordinary skill in the art to use the POR reset circuit of Rosa in the voltage detecting circuit for the purpose of incorporating a POR circuit into an integrated circuit that can receive different supply voltage.”

Even assuming *arguendo* that this is a reason to include the POR circuit of La Rosa in Matsumura, this would provide no apparent reason whatsoever to use the POR signal of La Rosa in place of the reset signal 52 of Matsumura. At most, the combination would use the POR circuit of La Rosa for the standard functionality of a POR circuit, but the POR signal would not be used in place of the reset signal 52 of Matsumura. Reset signal 52 of Matsumura is a reset signal, not a power-on reset signal. The two are not interchangeable, and the proposed replacement would change a principle of operation of the circuit of Matsumura.

As stated in DECLARATION OF Perry Scott Lorenz UNDER 37 C.F.R. § 1.132, Declaration 18, “The term ‘power on reset signal’ has a specific meaning to an engineer of ordinary competence in the field of analog circuit design, and is not arbitrary in meaning. To refer to any arbitrary signal as a ‘power on reset’ signal would not be considered reasonable to an engineer of ordinary competence in the field of analog circuit design.” It is respectfully submitted that the Office appears to be doing hindsight reconstruction based on the word “reset” and simply assumes,

in order to achieve hindsight reconstruction, that the reset signal 52 of Matsumura is interchangeable with the power on reset signal of La Rosa simply because they both contain the word “reset”. However, they are not interchangeable, and there is no apparent reason for the proposed substitution. The proposed substitution appears to be solely based on hindsight reconstruction based on the presence of the word “reset”, which is not an apparent reason for the proposed substitution.

Claims 2-12, 24, and 25 are respectfully submitted to be allowable at least because they depend from Claim 1.

Additionally, Claim 11 is respectfully submitted to be allowable at least because the proposed combination fails to meet the recitation, “the hysteresis-and-output-sensor circuit is configured to provide a hysteresis current to the resistor if the output signal corresponds to the second logic level”, as recited in Claim 11, in conjunction with the recitation, “the reference circuit includes: a resistor that is coupled to the hysteresis-and-output-sensor circuit and the comparator circuit; and a current source circuit that is configured to provide a current to the resistor”, as recited in Claim 10, from which Claim 11 depends. The Office Action states that component (5) of FIG. 1 of Matsumura is configured to provide a hysteresis current to the resistor in component (1) of FIG. 1 of Matsumura. However, Claim 10 recites that the resistor is in the reference circuit, and the resistor in component (1) of FIG. 1 of Matsumura is not in reference voltage circuit 2 of Matsumura.

Additionally, Claim 25 is respectfully submitted to be allowable at least because neither Matsumura nor La Rosa, singly or in combination, discloses, teaches, or suggests, “the hysteresis-and-output-sensor circuit is arranged to provide the hysteresis in a range of about 2°C to about 10°C of hysteresis for the temperature comparison when the hysteresis is enabled”, as recited in Applicant’s Claim 25. The proposed combination fails to teach temperature comparison, let alone temperature hysteresis, let alone temperature hysteresis in a range of about 2°C to about 10°C of hysteresis for the temperature comparison. The Office Action cites *In re Miller*. First, it is respectfully submitted that the Office mis-applies the *In re Miller* decision to an extreme degree. Second, the “working conditions” of Claim 25 are clearly not disclosed in the asserted prior art, since the asserted prior art has nothing to do with hysteresis of a temperature comparison. Third, no degree of optimization would modification the circuit of Matsumura to perform hysteresis in a range

of about 2°C to about 10°C of hysteresis for the temperature comparison, since Matsumura fails to perform temperature comparison or temperature hysteresis at all—therefore it cannot be optimized or adjusted to a workable range of a particular temperature range since a temperature range is not even applicable to the circuit of Matsumura.

Claim 13 is respectfully submitted to be allowable at least because none of the cited references, singly or in combination, teach any of the recitations of Claim 13.

First, none of the asserted references, singly or in combination, disclose, teach, or suggest, “employing a circuit to activate hysteresis if a temperature-sensing condition has occurred”. The Office asserts that component (5) of Matsumura activates hysteresis if a temperature-sensing is occurred. However, it is respectfully submitted that it is apparent that Matsumura does not disclose activating hysteresis if a temperature-sensing condition has occurred, or even perform any temperature sensing at all.

It is respectfully submitted that the Office has repeatedly attempted to meet the claim recitations based on circuits that do not perform temperature sensing at all. For example, previously, the Azimi circuit was asserted as performing a temperature sensing condition based on the assertion that a bandgap reference voltage is a temperature signal. As stated in DECLARATION OF Perry Scott Lorenz UNDER 37 C.F.R. § 1.132, Declaration 11, “An analog circuit engineer of reasonable competence could not reasonably construe the voltage at node 24 in Azimi, output by bandgap reference voltage 22, as a temperature sensor signal, even when the term ‘temperature sensor signal’ is construed as broadly as possible to one ordinary competence in the field of analog circuit design.” As stated in DECLARATION OF Perry Scott Lorenz UNDER 37 C.F.R. § 1.132, Declaration 13, “The circuit of Azimi does not activate hysteresis based on whether a temperature sensing condition of any type has occurred.” As stated in DECLARATION OF Perry Scott Lorenz UNDER 37 C.F.R. § 1.132, Declaration 15, “The comparator 27 of Azimi does not trip based on the temperature reaching a predetermined level. Furthermore, the comparison performed by comparator 27 of Azimi could not reasonably be construed as a temperature comparison.” As stated in DECLARATION OF Perry Scott Lorenz UNDER 37 C.F.R. § 1.132, Declaration 17, “Voltage Vin2 of Lin is not a temperature sensor signal.” As stated in DECLARATION OF Perry Scott Lorenz UNDER 37 C.F.R. § 1.132, Declaration 20, “The circuit

of Lim does not activate hysteresis based on whether a temperature sensing condition of any type has occurred.” As stated in DECLARATION OF Perry Scott Lorenz UNDER 37 C.F.R. § 1.132, Declaration 22, “In the circuit of Lim, the comparator OP20 tripping is not based on a temperature comparison, and is not based on the temperature reaching a predetermined level, but is instead based on whether Vin1 is high or low and on the hysteresis operation as illustrated in FIG. 5A-5E. The temperature could remain constant throughout the entire operation of the circuit of Lim, and comparator OP20 would still trip. A comparator that trips despite the temperature remaining constant throughout its operation could not reasonably be considered to be a temperature comparison under the broadest definition of the term ‘temperature comparison’ that would be considered reasonable to an engineer of ordinary competence in the field of analog circuit design.”

Although the declaration cited above discussed the Azimi and Lim references rather than the Matsumura reference, the same arguments apply to Matsumura. The circuit of Matsumura does not activate hysteresis based on whether or not a temperature sensing condition has occurred. Instead, the hysteresis is based on whether the detecting input voltage Vin is detected as a low voltage or a high voltage. The hysteresis is not based on whether or not a temperature condition has occurred, just as the Azimi and Lim references are not based on whether or not a temperature condition has occurred.

Second, none of the asserted references, singly or in combination, disclose, teach, or suggest, “ensuring that the hysteresis is automatically inactive when the circuit is powering up”.

Claims 14-19 and 26-34 are respectfully submitted to be allowable at least because they depend from Claim 13. Claim 20 is respectfully submitted to be allowable at least for reasons similar to those stated above with regard to Claim 13.

Additionally, Claim 32 is respectfully submitted to be allowable at least because the proposed combination fails to teach, “the temperature sensor signal is proportional to a temperature”. Input voltage Vin of Matsumura is not a temperature sensor signal, nor is it indicative of a temperature sensor signal, and it is respectfully submitted that it is absurd to assert that it is proportional to temperature. However, the Office simply asserts that it is with no support for the assertion. It is respectfully submitted that the burden is on the Office to make a prima facie

case that input voltage  $V_{in}$  is proportional to temperature. However, it is respectfully submitted that no prima facie case has been made, only a conclusory statement that it is.

Additionally, Claim 33 is respectfully submitted to be allowable at least because the proposed combination fails to teach, “the temperature-sensing condition is a temperature comparison in which a determination is made as to whether a temperature has reached a predetermined level”.

As stated in DECLARATION OF Perry Scott Lorenz UNDER 37 C.F.R. § 1.132, Declaration 22, “In the circuit of Lim, the comparator OP20 tripping is not based on a temperature comparison, and is not based on the temperature reaching a predetermined level, but is instead based on whether  $V_{in1}$  is high or low and on the hysteresis operation as illustrated in FIG. 5A-5E. The temperature could remain constant throughout the entire operation of the circuit of Lim, and comparator OP20 would still trip. A comparator that trips despite the temperature remaining constant throughout its operation could not reasonably be considered to be a temperature comparison under the broadest definition of the term ‘temperature comparison’ that would be considered reasonable to an engineer of ordinary competence in the field of analog circuit design.”

This declaration was made with regard to Lim, but the exact same argument applies with regard to Matsumura. The comparator 3 of Matsumura trips based on the detected voltage  $V_{in}$ . The temperature could remain constant throughout the entire operation of the circuit of Matsumura, and comparator 3 would still trip. A comparator that trips despite the temperature remaining constant throughout its operation could not reasonably be considered to be a temperature comparison under the broadest definition of the term “temperature comparison” that would be considered reasonable to an engineer of ordinary competence in the field of analog circuit design.

Additionally, Claim 26 is respectfully submitted to be allowable at least because the asserted art fails to teach, singly or in combination, “activating at least one of a fan or a heater when the output signal is asserted”, as recited in Applicants’ Claim 26.

The Office stated, “With respect to claim 26, this is deemed intended use, because no structural difference would be apparent.”

It is respectfully submitted that “structural difference” is irrelevant since claim 26 is a **method** claim. It can be argued that a functional statement of intended use in an apparatus claim

may be ignored as intended use on the basis that there is no structural difference between the claimed apparatus and the prior art. For example, as stated in case law previously quoted by the Office in this matter, “it has been held that a recitation with respect to the manner in which a claimed apparatus is intended to be employed does not differentiate *the claimed apparatus* from a prior art apparatus satisfying the claimed structural limitations.” (emphasis added). However, Claim 26 is a claimed method not a *claimed apparatus*. Because it is a claimed method, the recited elements of Claim 26 cannot be rejected on the basis of intended use. The recitations in claims 26 and 28-30 are claimed steps in a method, and not mere intended uses of a claimed apparatus. Accordingly, the Office Action’s assertions regarding “intended use” limitations not having patentable weight does not apply. An explicitly claimed step or act in a method claim cannot simply be ignored on the basis that the recited step or act is “intended use”. Rather, each act or step recited in the claim must be considered by the Office.

Further, it is respectfully submitted that Applicant’s representative has repeatedly pointed this fact out regarded “intended use” arguments regarding Claims 26 and 28-30 in previous responses. By making a repeated rejection Claim 26 and 28-30 on the basis of “intended use” while ignoring Applicant’s arguments that the recitations in claims 26 and 28-30 are claimed steps in a method, and not mere intended uses of a claimed apparatus, it is respectfully submitted that the Office fails to advance prosecution forward on those in claims in a meaningful manner. In fact, this moves prosecution backward since Claims 26 and 28-30 were previous identified as having allowable subject matter based on Applicant’s representative’s previous arguments that ignored claimed method steps or acts on the grounds that they are “intended use” is improper (See Office Action dated May 21, 2009).

Claim 22 is respectfully submitted to be allowable at least for reasons similar to those stated above with regard to Claim 1. Additionally, Claim 22 is respectfully submitted to be allowable at least because the proposed combination fails to teach, “a comparator circuit that is arranged to provide a trigger signal by comparing a reference signal to a temperature sensor signal”, as recited in Applicants’ Claim 22. As discussed in greater detail above, voltage  $V_{in}$  of Matsumura is not a temperature sensor signal.

Claim 22 is respectfully submitted to be allowable for at least the reasons stated above, and notice to that effect is earnestly solicited. Claims 21 and 23 are respectfully submitted to be allowable at least because they depend on Claim 22.

### New discussion

Applicant made similar arguments to those stated above in a previous paper. Applicant's remarks regarding the Office's response to arguments are discussed in detail below.

#### I)

With regard to Applicant's argument that neither Matsumura nor La Rosa, singly or in combination, discloses, teaches, or suggests, "the hysteresis-and-output sensor circuit is arranged to disable a hysteresis at power up", as recited in Applicant's Claim 1, the Office disagrees, arguing that the claim merely states the arrangement, and that the combination is clearly capable of disabling hysteresis at power up.

Applicant respectfully disagrees. Claim 1 does refer to structure, but it refers to structure that definitely performs the function under the recited circumstances. In other words, Claim 1 requires the hysteresis-and-output sensor circuit disable the hysteresis at power up under the condition that power up occurs. If a device is so structured that it would disable the hysteresis at power up, but the device is never powered up, the device still meet the recited claim features because it has the recited structure. But if a device could be completely re-designed so as to disable the hysteresis at power up, but as currently designed does not disable hysteresis when it is powered up, it does not meet the recited claim features.

However, the discussion above is moot, since the combination is not capable of disabling hysteresis at power up. In order to disable hysteresis at power up, the combination would have to be re-designed in such a way that it detected that power up was occurring, have this detection provided to circuitry that controls the hysteresis, and have circuitry that is capable of turning on and off hysteresis based on the detection that power up is occurring. Since the combination does not have such circuitry and structure, it is not capable of disabling hysteresis at power up. For example, if the



circuit of Matsumura were modified to include the POR circuit of La Rosa, the combination would still not disable hysteresis at power up; reset signal 52 of Matsumura would not be based on the POR signal in any way. The reset functionality of reset signal 52 of Matsumura is not based on power up in any way. The Office proposes to replace Reset signal 52 of Matsumura with a POR signal, but as discussed elsewhere, there is no apparent reason to make such a replacement, and such a replacement would change a principle of operation of Matsumura.

## II)

The Office also disagreed with Applicant's argument that the circuit of La Rosa fails to cure the deficiency of Matsumura. The Office stated that the adjusting circuit of Matsumura would be able to adjust the La Rosa input to disable it at power supply, that Matsumura disables hysteresis since it is pulled to the ground at power-up, and that the time period of power-up is not clearly defined in the specification or claim language.

Applicant respectfully disagrees. Even if the POR circuit of La Rosa were used in Matsumura, the circuit of Matsumura still would not disable POR at power-up. In the asserted art, there is no apparent reason or suggestion that the hysteresis would be based on power-up in any way. Accordingly, even if the circuit of Matsumura incorporated the POR of Matsumura, the combination would still fail to disable hysteresis at power up. Including the POR circuit of La Rosa in Matsumura would not make it obvious to disable the hysteresis at power up. There is no apparent reason in the asserted references to modify the circuit of Matsumura to disable the hysteresis at power up. Incorporating a POR circuit in Matsumura would not result in the circuit of Matsumura disabling hysteresis at power up. The Office states that the adjusting circuit of Matsumura would "be able to adjust the La Rosa input to disable it at power up", but there is no apparent reason to modify the circuit of Matsumura to disable the hysteresis at power up. Reset signal 52 of Matsumura is a reset signal, not a power-on reset signal. The two are not interchangeable, and the proposed replacement would change a principle of operation of the circuit of Matsumura.

Further, the circuit of Matsumura does not disable hysteresis during power-up. The circuit of Matsumura is pulled to ground **prior** to power up; it is not pulled to ground **during** power up. Also, the time period of power up does not need to be clearly defined in the specification or claims

because the meaning of the term is understood by one of ordinary skill in the art, and the meaning is clear from the overall explanation of the invention given in the specification. As stated in DECLARATION OF Perry Scott Lorenz UNDER 37 C.F.R. § 1.132, Declaration 19, “To an engineer of ordinary competence in the field of analog circuit design, ‘power up’ or ‘when the circuit is powering up’ is not an arbitrary term, and has a specific meaning and occurs at a specific time, rather than any arbitrarily designated time. To refer to ‘power up’ as occurring at any arbitrarily designated time would not be considered reasonable to an engineer of ordinary competence in the field of analog circuit design.”

### III)

The Office also argues that it would have been obvious to use a POR signal in place of reset signal 52 of Matsumura. Applicant respectfully disagrees.

The Office states, “A POR reset signal is still interpreted as a reset signal that can replace a standard reset signal at power up. The only difference between a reset signal and a power on reset signal is that a power on reset signal is operative at the power up of the device. A reset signal can come from any device and it is not hindsight to use a reset signal from a POR device....” Applicant respectfully disagrees. It is simply not the case that the only difference between a reset signal and a power on reset signal is that a power on reset signal is operative at the power up of the device. Reset signals can be used for a variety of different purposes in different circuitry. A reset signal is used to reset something, but the times in which the reset signal is asserted in order to initiate the reset depends on the specific circuitry. A power on reset signal is used to perform a reset at power up. A power on reset signal is always asserted at power up, and it is never asserted at any other time. The reset signal 52 of Matsumura is not asserted at power up, and is asserted at other specific times other than power up. To substitute one for the other (a POR signal for reset signal 52 of Matsumura) would completely change the functionality of Matsumura and render it unsuitable for its intended purpose. As stated in DECLARATION OF Perry Scott Lorenz UNDER 37 C.F.R. § 1.132, Declaration 18, “The term ‘power on reset signal’ has a specific meaning to an engineer of ordinary competence in the field of analog circuit design, and is not arbitrary in meaning. To refer

to any arbitrary signal as a 'power on reset' signal would not be considered reasonable to an engineer of ordinary competence in the field of analog circuit design."

IV)

Regarding Claim 11, the Office states, "It would have been obvious at the tie to use the voltage source of Matsumura coupled to a variable resistor for the purpose of adjusting the voltage reference. Thus the resulting combination would produce the claim of claim 10."

Applicant respectfully disagrees. Even of the voltage source of Matsumura included a variable resistor of the purpose of adjusting the voltage reference, it would not "provide a hysteresis current to the resistor if the output signal correspond to the second logic level."

V)

With regard to Claim 25, the Office states that "the workable range is obvious expedient". It is respectfully submitted that Applicant has already thoroughly addressed the Office's argument that "the workable range is obvious expedient" and that the Office has not addressed these arguments at all. Among other things, there is no way to modify the proposed combination to reach the "workable range" of Claim 25--no degree of optimization would modify the circuit of Matsumura to perform hysteresis in a range of about 2°C to about 10°C of hysteresis for the temperature comparison, since Matsumura fails to perform temperature comparison or temperature hysteresis at all—therefore it cannot be optimized or adjusted to a workable range of a particular temperature range since a temperature range is not even applicable to the circuit of Matsumura.

VI)

The Office states, with respect to claim 13, with regard to the recitation, "employing a circuit to activate hysteresis if a temperature sensing condition has occurred", that "the temperature sensor of the claimed invention is shown not with structure but only as an input and thus not given patentable weight since it is interpreted only as a voltage value."

Applicant respectfully points out that Claim 13 is a method step and that the step “employing a circuit to activate hysteresis if a temperature sensing condition has occurred” is a method step that is specifically an element of the claimed invention claimed by Claim 13.

Applicant respectfully submits that by not giving patentable weight to the claim element, “employing a circuit to activate hysteresis if a temperature sensing condition has occurred”, the Office has violated the all elements rule.

According to the all elements rule, a claim is anticipated only if each and every element as set forth in the claim is found, either expressly or inherently found, in a single prior art reference. MPEP 2131. The all elements rule also applies in obviousness analysis. In obviousness analysis, under the all elements rule, for claimed subject matter to be obvious, either the prior art references must expressly teach each claim element exactly or the record must disclose a reason for a person of ordinary skill in the art to modify the prior art teachings to obtain the claimed invention. *Beckson Marine, Inc. v. NFM, Inc.*, 292 F.3d 718, 727 (Fed. Cir. 2002). All of the claim limitation must be considered in judging the patentability of a claim. MPEP 2143.03.

As discussed above, the asserted combination must also teach or suggest *each and every claim feature*. See *In re Royka*, 490 F.2d 981, 180 USPQ 580 (CCPA 1974) (emphasis added) (to establish *prima facie* obviousness of a claimed invention, all the claim features must be taught or suggested by the prior art). Indeed, as the Board of Patent Appeal and Interferences has confirmed, a proper obviousness determination requires that an Examiner make “a searching comparison of the claimed invention – *including all its limitations* – with the teaching of the prior art.” See *In re Wada and Murphy*, Appeal 2007-3733, citing *In re Ochiai*, 71 F.3d 1565, 1572 (Fed. Cir. 1995) (emphasis in original). Further, the necessary presence of all claim features is axiomatic, since the Supreme Court has long held that obviousness is a question of law based on underlying factual inquiries, including ... ascertaining the differences between *the claimed invention* and the prior art. *Graham v. John Deere Co.*, 383 U.S. 1, 148 USPQ 459 (1966) (emphasis added). Indeed, Applicant submits that this is why Section 904 of the MPEP instructs Examiners to conduct an art search that covers “the invention *as described and claimed*,” (emphasis added). Lastly, Applicant respectfully directs attention to MPEP § 2143, the instructions of which buttress the conclusion that obviousness requires at least a suggestion of all of the features of a claim, since the Supreme Court in *KSR Int'l*

*v. Teleflex Inc.* stated that “there must be some articulated reasoning with some rational underpinning to support the legal conclusion of obviousness.” *KSR Int’l v. Teleflex Inc.*, 127 S. Ct. 1727, 1741 (2007) (quoting *In re Kahn*, 441 F.3d 977, 988 (Fed. Cir. 2006)).

It remains well-settled law that obviousness requires at least a suggestion of all of the features in a claim. See *In re Wada and Murphy*, citing *CFMT, Inc. v. Yieldup Intern. Corp.*, 349 F.3d 1333, 1342 (Fed. Cir. 2003) and *In re Royka*, 490 F.2d 981, 985 (CCPA 1974)).

Accordingly, it is respectfully submitted that the Office must give patentable weight to the explicitly recited method step in Claim 13 of “employing a circuit to activate hysteresis if a temperature sensing condition has occurred”, and that with this step given patentable weight, Claim 13 is in condition for allowance.

Similar arguments apply to Claim 32, in which the Office also fails to give patentable weight to a claim element.

## VII)

With regard to Claim 26, the Office states, “With respect to Claim 26, the claimed is deemed to be intended use”. For reasons similar to those discussed above with regard to Claim 13, the recited claim step, “activating at least one of a fan or a heater when the output signal is asserted” must be given patentable weight under the all elements rule. Further, the Office’s discussion regarding “intended use” does not apply to the specifically recited claim element steps of a method claim.

Further, the signal output by the proposed combination would not be a signal that is based on a temperature comparison, nor has the Office proposed a way to modify the combination do so, and therefore there would be no apparent reason to use the signal to activate a fan or a heater.

As stated in DECLARATION OF Perry Scott Lorenz UNDER 37 C.F.R. § 1.132, Declaration 22, “In the circuit of Lim, the comparator OP20 tripping is not based on a temperature comparison, and is not based on the temperature reaching a predetermined level, but is instead based on whether Vin1 is high or low and on the hysteresis operation as illustrated in FIG. 5A-5E. The temperature could remain constant throughout the entire operation of the circuit of Lim, and comparator OP20 would still trip. A comparator that trips despite the temperature remaining

constant throughout its operation could not reasonably be considered to be a temperature comparison under the broadest definition of the term ‘temperature comparison’ that would be considered reasonable to an engineer of ordinary competence in the field of analog circuit design.”

This declaration was made with regard to Lim, but the exact same argument applies with regard to Matsumura. The comparator 3 of Matsumura trips based on the detected voltage  $V_{in}$ . The temperature could remain constant throughout the entire operation of the circuit of Matsumura, and comparator 3 would still trip. A comparator that trips despite the temperature remaining constant throughout its operation could not reasonably be considered to be a temperature comparison under the broadest definition of the term “temperature comparison” that would be considered reasonable to an engineer of ordinary competence in the field of analog circuit design. Since the output signal of the combination is not based on a temperature comparison, there is no apparent reason to use the signal to activate a fan or a heater. Since the temperature could remain constant throughout the entire operation of the circuit of Matsumura, and comparator 3 would still trip, it would be irrational to use the output of comparator 3 of Matsumura to activate a fan or heater.

#### VIII)

The Office has not addressed certain arguments made by Applicant.

For example, Applicant’s arguments regarding Claim 22 have not been addressed.

With regard to Claim 33, Applicant’s specific arguments have not been addressed, although the Office does state, “With respect to claims 14-19 and 26-34, these are rejected for similar reasons”. It appears that the Office is refusing to give patentable weight to the further recitations of Claim 33. It is respectfully submitted that the Office must give patentable weight to the further recitations of Claim 33 under the all elements rule.

**CONCLUSION**

It is respectfully submitted that each of the presently pending claims (Claims 1-34) is in condition for allowance and notification to that effect is requested. Examiner is invited to contact the Applicant's representative at the below-listed telephone number if it is believed that the prosecution of this application may be assisted thereby. Although only certain arguments regarding patentability are set forth herein, there may be other arguments and reasons why the claimed invention is patentable. Applicant reserves the right to raise these arguments in the future.

Payment in the amount of \$1110.00 covering the fee set forth in 37 CFR 1.136(a) is included herewith. It is believed that no further fees for this submission are necessary; however, the Commissioner is hereby authorized and requested to charge any deficiency or credit any excess to Deposit Account No. 50-0320.

Dated: September 28, 2010

Respectfully submitted,

By 

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